



5659 2800

533 Rec'd PCT/PTO 17 SEP 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In ~~the~~ patent application of:
Reginald Leeman et al.

Before the Examiner

2841

Serial No. 09/831,415

Filed: May 9, 2001

International Filing Date: November 2, 1999)

Group Art Unit:

Electrical Components for Printed Boards)
and Method for Automatically Inserting Said)
Components in Printed Boards)

#7
FDJ
L. Hudson
2-2802

RECEIVED
OCT 26 2001
TC 2800 MAIL ROOM

INFORMATION DISCLOSURE STATEMENT

Applicants hereby submit the attached documents listed on the attached PTO Form 1449 in accordance with 37 CFR §1.97(b)(3), as these documents are being submitted before the first Office Action. Copies of the cited items are enclosed in accordance with 37 C.F.R. § 1.98.

The filing of this Information Disclosure Statement shall not be construed as an admission that the information cited is, or is considered to be, material to patentability as defined in § 1.56(b), or that these documents are prior art.

Two references, that is, DE19716139 and EP0582960, are enclosed herein and are only available in the German language. Both of these references are referred to in the specification, that is, on page 1 of 12 in the case of EP0582960, and on page 2 of 12 in the case of DE19716139.

With reference to EP0582960, and particularly Figure 2, a multiport coaxial connector is shown comprised of two mating components. Figure 3 shows a cross sectional view through the right angled header which is mounted to circuit board 5. As shown in Figure 3, an outer conductive block is shown at 7, which forms a plurality of ports 6 to receive the mating coaxial connectors. Compliant or press fit pin sections 13 form the electrical connection to the pins 8, whereas compliant pin portions 14 form the electrical connection to the conductive block 7.

DE19716139 shows a coaxial plug module comprised of coaxial plugs at 4 with the plurality of surface mount device (SMD) style contacts at 5. SMD contacts 5, 6 serve to both electrically connect socket module 1 to the printed circuit board as well as retain the module 1 thereto. The module 1 further comprises positioning pins 28 for cooperation with corresponding positioning holes in a printed circuit board.

It is believed that no fee is required for consideration of the submitted items. Should any fee be required, however, please charge such fee to Deposit Account No. 02-0387, but not to include any payment of issue fees.

Respectfully submitted,



Eric J. Groen, Reg. No. 32,230
BAKER & DANIELS
205 West Jefferson Blvd., Suite 250
South Bend, IN 46601
(219) 234-4149

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231 on:
September 14, 2001



Michelle L. Watson

Dated: September 14, 2001